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NOTICE OF ALLOWANCE AND FEE(S) DUE

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12/30/2008

MAGINOT, MOOR & BECK 111 MONUMENT CIRCLE, SUITE 3000 BANK ONE CENTER/TOWER INDIANAPOLIS, IN 46204 EXAMINER

MCKIE, GINA M

ART UNIT PAPER NUMBER

2611

DATE MAILED: 12/30/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/530,852	04/11/2005	Anthony Sanders		2280

10/530,852 04/11/2005 Anthony Sanders
TITLE OF INVENTION: CLOCK SIGNAL EXTRACTION DEVICE AND METHOD FOR EXTRACTION A CLOCK SIGNAL FROM DATA SIGNAL

APPLN. TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE nonprovisional NO \$1510 \$300 \$0 \$1810 03/30/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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10/530,852	04/11/2005	Anthony Sanders	2280	
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111 MONUMENT CIRCLE, SUITE 3000			ART UNIT	PAPER NUMBER
BANK ONE CEN' INDIANAPOLIS,			2611 DATE MAILED: 12/30/2008	

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 584 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 584 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Application No.	Applicant(s)				
10/530.852	SANDERS ET AL.				
Examiner	Art Unit				
GINA MCKIE	2611				
(OR REMAINS) CLOSED i or other appropriate comm GHTS . This application is	n this application. If not included unication will be mailed in due course.				
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ENT of this application. tted. Note the attached EX	AMINER'S AMENDMENT or NOTICE (
 5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. 					
6.	ummary (PTO-413), /Mail Date Amendment/Comment				
	Examiner GINA MCKIE ars on the cover sheet with (OR REMAINS) CLOSED in content appropriate comming GHTS. This application is stand MPEP 1308. der 35 U.S.C. § 119(a)-(d) been received. been received in Application been received in Application. and MPEP 1308. der 35 U.S.C. § 119(a)-(d) been received in Application been received in Application. atted. Note the attached EXAMINET of this application. atted. Note the attached EXAMINET of this application. atted. Note the attached EXAMINET of this application. attending the submitted of the submitted. and Mendment / Comment of the header according to 37 CHEST of BIOLOGICAL MATE of BIOLOGICAL MATE of THE DEPOSIT OF BIOLOGICAL MATE of THE DEPOSIT OF BIOLOGICAL MATE of BIOLOG	Examiner Art Unit 2611			

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DETAILED ACTION

Response to Amendment

1. Acknowledgement is made of the amendments filed June 30, 2008 and September 02, 2008. Claims 17-36 remain pending in the application.

• Claims 17, 18, 24, 25, and 29 have been amended.

• No claims have been canceled.

No claims are new.

Response to Arguments

Drawings

2. Applicant's arguments, see page 9 of REMARKS, filed June 30, 2008, with respect to the objection to figure 1 for lacking word labels, have been fully considered. The objection to the drawings has been withdrawn in view of the replacement drawing submitted June 30, 2008.

Specification

3. Applicant's arguments, see page 9 of REMARKS, filed June 30, 2008, with respect to the objection to the specification, including the abstract of the disclosure for containing legal phraseology, have been fully considered. The objection to the specification has been withdrawn in view of the replacement abstract submitted September 02, 2008.

Claim Rejections – 35 USC § 112, first paragraph

4. Applicant's arguments, see pages 9-10 of REMARKS, filed June 30, 2008, with respect to the rejections of claims 17, 24, and 29 under 35 USC § 112, first paragraph as failing to comply with the enablement requirement have been fully

considered. The 35 USC § 112, first paragraph rejections of claims 17, 24, and 29 have been withdrawn in view of the amendment to the claims filed June 30, 2008.

Claim Rejections – 35 USC § 103(a)

5. Applicant's arguments, see pages 10-12 of REMARKS, filed June 30, 2008, with respect to the rejection of independent claims 17, 24, and 29 under 35 USC § 103(a) as being unpatentable over Nakano (US 5,745,468) in view of Aoki et al. (US 6,236,696) have been fully considered and are persuasive. The 35 USC § 103(a) rejection of claims 17, 24, and 29 has been withdrawn.

As such, claims 17, 24, 29, and their dependents therefrom are put in condition for allowance.

EXAMINER'S AMENDMENT

6. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Russell Fowler on December 19, 2008.

The claims in the application have been amended as follows:

(1) Claim 17:

A clock signal extraction device for extracting an extracted clock signal out of a periodic data signal, comprising:

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a first clock generator circuit configured to generate a rising edge clock signal, the first clock generator circuit having a first output;

a first phase detector configured to detect a first phase difference between a rising edge of the periodic data signal and a rising edge of the rising edge clock signal, the first phase detector having a first input connected to the first output to form a first loop, and wherein the first clock generator circuit and the first phase detector cooperate to reduce the detected first phase difference,

a second clock generator circuit configured to generate a falling edge clock signal, the second clock generator circuit having a second output;

a second phase detector configured to detect a second phase difference between a falling edge of the **periodic** data signal and a falling edge of the falling edge clock signal, the second phase generator having a second input connected to the second output to form a second loop, and wherein the second clock generator circuit and the second phase detector cooperate to reduce the detected second phase difference;

a third clock generator circuit configured to generate the extracted clock signal and;

a controller comprising a phase pump and a loop filter configured to control the third clock generator <u>circuit</u> based on an average of the first phase difference and the second phase difference.

(2) Claim 18:

The clock signal extraction device according to claim 17, wherein the controller is configured to process said first phase difference and said second

phase difference to control generation of the extracted clock signal by the third clock generator **circuit**.

(3) Claim 24:

An arrangement for extracting data, including:

a clock signal extraction device for extracting an extracted clock signal out of a periodic data signal, comprising

a first clock generator circuit configured to generate a rising edge clock signal, the first clock generator circuit having a first output,

a first phase detector configured to detect a first phase difference between a rising edge of the periodic data signal and a rising edge of the rising edge clock signal, the first phase detector having a first input connected to the first output to form a first loop, and wherein the first clock generator circuit and the first phase detector cooperate to reduce the detected first phase difference,

a second clock generator circuit configured to generate a falling edge clock signal, the second clock generator circuit having a second output;

a second phase detector configured to detect a second phase difference between a falling edge of the **periodic** data signal and a falling edge of the falling edge clock signal, the second phase generator having a second input connected to the second output to form a second loop, and wherein the second clock generator circuit and the second phase detector cooperate to reduce the detected second phase difference,

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a third clock generator circuit configured to generate the extracted clock signal and a controller comprising a phase pump and a loop filter configured to control the third clock generator **circuit** based on an average of the first phase difference and the second phase difference; and a data extraction device configured to extract data from said **periodic** data signal according to a rate of said extracted clock signal.

(4) Claim 25:

The arrangement according to claim 24, wherein the controller is configured to process said first phase difference and said second phase difference to control generation of the extracted clock signal by the third clock generator **circuit**.

(5) Claim 26:

The arrangement according to claim 25, wherein said controller controls said third clock generator **periodic** to generate said crock signal such that the error rate of the extracted data is minimized.

(6) Claim 27:

The arrangement according to claim 24, wherein said data extraction device comprises a data sampler for sampling said **circuit** data signal.

(7) Claim 29:

A method for extracting an extracted clock signal out of a periodic data signal, comprising:

(a1) generating a rising edge dock signal;

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(a2) detecting a first phase difference between a rising edge of the periodic data signal and **the** [[a]] rising edge of a rising edge clock signal;

- (a3) feeding back the first phase difference to generate a subsequent rising edge clock signal having a reduced first phase difference;
 - (b1) generating a falling edge clock signal;
- (b2) detecting a second phase difference between a falling edge of the periodic data signal and a falling edge of the [[a]] falling edge clock signal;
- (b3) feeding back the second phase difference to generate a subsequent falling edge clock signal having a reduced second phase difference; and
- (c) generating the extracted clock signal and controlling the extracted clock signal based on an average of a of the first phase difference and the second phase difference way using a phase pump and a loop filter.

(8) Claim 32:

The method according to claim 29, further comprising a step of: extracting data from said **periodic** data signal according to a rate of said extracted clock signal.

(9) Claim 34:

The method according to claim 33, wherein said **periodic** data signal is an optical **periodic** data signal.

(10) Claim 35:

The method according to claim 33, further comprising extracting data from said **periodic** data signal using a flip-flop.

(11) Claim 36:

The method according to claim 29, wherein said **periodic** data signal is an optical **periodic** data signal.

REASONS FOR ALLOWANCE

- 7. Claims 17-36 are allowed.
- 8. The following is an examiner's statement of reasons for allowance:

 The prior art of record fails to teach, alone or in combination,

"...generating the extracted clock signal and controlling the extracted clock signal based on an average of the first phase difference and the second phase difference using a phase pump and a loop filter," as claimed in claims 17, 24, and 29.

Aoki et al. (US 6,236,696) is an exemplary reference from the relevant subclasses. However, Aoki discloses a duty judging circuit which generates duty information used by a data selector to judge an optimal phase suitable for a data decision. The duty judging circuit in Aoki does not use a first phase difference and a second phase difference for calculating the duty cycle. Instead, Aoki uses the absolute phase of a falling edge and the absolute phase of a rising edge forwarded to a subtracter. The subctracters calculate a duration of logical high or logical low level. The duty judging circuit of Aoki does not satisfy the claim limitations of Applicant's claims 17, 24, and 29 alone, nor in combination with Nakano (US 5,745,468) (Nakano being used to satisfy other limitations of the claims). Therefore claims 17, 24, 29, and their dependents therefrom contain allowable subject matter.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to GINA MCKIE whose telephone number is (571)270-5148. The examiner can normally be reached on Mon-Fri, 9:00 AM-4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shuwang Liu can be reached on 571-272-3036. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/Gina McKie/ Examiner, Art Unit 2611 /Shuwang Liu/ Supervisory Patent Examiner, Art Unit 2611